



**PATENT**  
5500-88700/TT5374

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/726,902

Filed: December 3, 2003

Inventors:

Mitchell Alsup

Gregory William Smaus

**Title:** Transitioning From  
Instruction Cache to Trace  
Cache on Label Boundaries

Examiner: Fennema, Robert E  
Group/Art Unit: 2183  
Atty. Dkt. No: 5500-88700

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name

Printed Name \_\_\_\_\_  
Signature \_\_\_\_\_

May 26, 2006

Date \_\_\_\_\_

**RESPONSE TO OFFICE ACTION OF FEBRUARY 28, 2006**

## Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of February 28, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.